



# High Performance Analog and Power Management Circuit Design Techniques for Modern SoCs

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## High Performance Analog and Power Management Circuit Design Techniques for Modern SoCs

This book illustrates some of the recent state-of-the-art advances in Analog and Power Management circuit design. Coverage includes design of advanced low-power/low-voltage analog circuits, small-signal and pole-zero analysis of multi-stage amplifiers, state-of-the-art frequency compensation topologies, and advanced power management circuits and control techniques. Readers will benefit from detailed small-signal techniques for complex multi-stage amplifiers and low drop-out voltage regulators (LDOs), a ubiquitous power management circuit. The authors provide tutorial treatment of frequency compensation techniques while illustrating some of the state-of-the-art techniques. The authors also discuss in detail the challenges in the design of modern power management circuits, including low dropout voltage regulators, switched-capacitor DC-to-DC converters and inductor-based DC-DC converters.



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